AMENDMENTS TO THE SPECIFICATION

In the Specification

Please delete the paragraph beginning on page 1, line 10 and insert the following in place thereof:

-- Multimedia applications such as 2D/3D graphics, image processing, video compression/decompression, voice recognition algorithms and audio manipulation, often require the same operation to be performed on a large number of data items (referred to as "data parallelism"). Each type of multimedia application typically implements one or more algorithms requiring a number of floating point or integer operations, such as ADD or MULTIPLY (hereafter MUL). By providing macro instructions whose execution causes a processor to perform the same operation on multiple data items in parallel, Single Instruction Multiple Data (SIMD) technology, such as that employed by the Pentium® processor architecture and the ~~MMx™~~ MMX^{TM}. instruction set, has enabled a significant improvement in multimedia application performance (Pentium® and ~~MMx™~~ MMX^{TM} are registered trademarks or trademarks of Intel Corporation of Santa Clara, ~~CA~~ California). --